

### **Amendments to the Specification**

To provide a A method for the self-testing of a reference voltage in electronic components, ~~by means of which method there is specified~~ includes a circuit arrangement for a self-test of the reference voltage that can be implemented in the form of an on-chip test, ~~i.e. e.g.,~~ for which no external reference voltage source is required, ~~provision is made for the.~~ The reference voltage ( $U_{ref}$ ) ~~to be~~ is fed to a voltage-controlled oscillator whose output forms the input to a Wien-Robinson bridge whose output signal is checked in a phase detector for its phase shift relative to the input to the Wien-Robinson bridge to check the balance of the Wien-Robinson bridge, ~~the.~~ The Wien-Robinson bridge ~~being~~ is set to be balanced at a frequency ( $\Omega_{ref.test}$ ) that is generated in the oscillator at the nominal value ( $U_{ref.test}$ ) selected for the reference voltage ( $U_{ref}$ ), and a pass signal is generated if the bridge is balanced and a fail signal is generated if it is not.